

Amendment to the Specification:

Please amend lines 1 through 19 on page 6 of the application as follows:

Figure 1A illustrates a standard configuration for ESD protection, according to the prior art;

Figure 1B shows the electrical characteristics of the standard ESD protection scheme of Figure 1A;

Figure 1C illustrates the drain current logarithmically, with respect to drain-source voltage, when gate voltage is held at zero, for a standard configuration for ESD protection according to the prior art;

Figure 2A illustrates a device for ESD protection, according to one embodiment of the present invention;

Figure 2B illustrates the electrical characteristics of the ESD protection device of Figure 2A;
and

Figure 3 illustrates an ESD protection scheme, according to the invention in an integrated circuit application.